# Reset

This PIN is used to reset the module.

1. LOW on this PIN puts the module in a reset stage
2. HIGH on this PIN releases the module from reset

During the reset stage, all the GPIOs must be LOW or in an undriven state. EN\_CHIP/RST must be held below 0.6V to reset device.

Figure 2 depicts the power-up sequence for Talaria TWO.

Graphical user interface, text, application

Description automatically generated

Figure 2: Power-up sequence

Figure 3 depicts the suggested reset circuit which can be used in the application boards. This reset circuit gives the module sufficient time delay to settle down after power-up or after a reset.

Diagram, schematic

Description automatically generated

Figure 3: Reset circuit